



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In Re the Application of: Jitaru
Serial #: 09/434,985
Filed: 11/05/99

Docket #: 1675B.1A.1
Examiner:
Group:

For: Low Noise Full Integrated Multilayers Magnetic For Power
Converters

FEE CALCULATION
FOR SECOND PRELIMINARY AMENDMENT

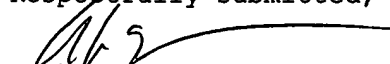
Honorable Commissioner of Patents and Trademarks
Washington D.C. 20231

Sir:

Payment is based on the following calculations (37 C.F.R. 1.16):

| | | |
|--|----|-------------|
| CURRENT NUMBER OF INDEPENDENT CLAIMS | 5 | |
| NUMBER OF INDEPENDENT CLAIMS PREVIOUSLY PAID | 3 | |
| Excess: | 2 | \$ 78.00 |
| (X) Small Entity @ \$39.00 each | | |
| () NOT Small Entity @ \$78.00 each | | |
| CURRENT NUMBER OF CLAIMS | 23 | |
| NUMBER OF CLAIMS PREVIOUSLY PAID | 20 | |
| Excess | 3 | \$ 27.00 |
| (X) Small Entity @ \$9.00 each | | |
| () NOT Small Entity @ \$18.00 each | | |
| CURRENT NUMBER OF MULTIPLE DEPENDENT CLAIMS | 0 | |
| NUMBER OF MULTIPLE INDEPENDENT CLAIMS PAID | 0 | |
| Excess | 0 | ... \$ 0.00 |
| (X) Small Entity @ \$130.00 | | |
| () NOT Small Entity @ \$260.00 | | |
| TOTAL: | | \$ 105.00 |

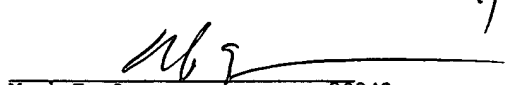
Respectfully Submitted,


Mark E. Ogram
Attorney at Law
Reg. No. 30343

Date: 1/19/2000

CERTIFICATE OF MAILING (37 CFR 1.8)

I hereby certify that this correspondence is being deposited with the United States Postal Service as first class mail in an envelope addressed to: Commissioner of Patents and Trademarks, Washington, D.C. 20231, on 1/19, 1992000


Mark E. Ogram, reg. no. 30343

Date 1/19/2000

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(106)
SB
8/14/00

SECOND PRELIMINARY AMENDMENT

Honorable Commissioner of Patents and Trademarks
Washington D.C. 20231

Sir:

Regarding the above identified patent application, please make the following amendments and note the corresponding remarks.

Enclosures:

- (1) Check number 3338 for the amount \$ 105.00 ;
(2) Fee Calculation Sheet for the Second Preliminary Amendment.

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In the Claims:

Add Claims 20- 23 as indicated below:

- 1 20. A power processing device comprising:
2 a) a multilayer printed circuit board having multiple layers of
3 dielectric sheets;
4 b) a transformer having,
5 1) a first core extending through said layers of dielectric
6 sheets, and,
7 2) a first set of electrically conductive windings, at least one
8 of said windings of said first set of electrically conductive
9 windings contained between two adjoining layers of said dielectric
10 sheets;
11 c) an output choke having,
12 1) a second core extending through said layers of dielectric

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